Transmitted herewith for filing is the Utility patent application of:

INVENTOR: YUTAKA OGAWA, KAZUTOMO OGURA, NAOFUMI SATOU AND KIYOTADA FUNANE FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Enclosed are:

- 4 sheets of □ formal □ informal drawings.
- Request for Priority to claim priority date of November 5, 2002.
- M An Executed Declaration.
- & An assignment executed by inventors to Renesas Technology Corp and Hitachi ULSI Systems Co., Ltd.

■ The filing fee is calculated as shown below:

For	NUMBER FILED		NUMBER EXTRA	RATE	Culow
TOTAL CLAIMS	17	-20	0	x\$18 =	CALCULATION 0.00
INDEPENDENT CLAIMS	3	-3	0	x\$84 ==	0.00
IULTIPLE DEPENDENT CLAIM(S) (if applicable) +\$280 =					1
BASIC FEE					\$750.00
DUCTION BY 1/2 FOR FILING BY SMALL ENTITY (DOI: 37 C.F.R. §§ 1.9, 1.27, 1.28).					750.00
DUCTONIENT					
DUCTION BY 1/2 FOR FILE APPLICABLE, VERIFIED S	TATEMENT ML	ST BE ATTACHE	7 C.F.R. §§ 1.9, 1.27, 1.28). D		

Please charge my Deposit Account Number fees. A duplicate copy of this paper is enclosed. _ in the amount of to cover the filing and assignment recordation

- A check in the amount of \$790.00 to cover the filing fee and assignment fee is enclosed.
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to Deposit Account Ø Respectfully submitted,

Stanley P. Fisher

REED SMITH LLP 3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 September 26, 2003

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Juan Carlos A. Marguez Registration Number 34 072